

**Notice of References Cited**

Application/Control No.

09/763,204

Applicant(s)/Patent Under  
Reexamination  
CLERMIDY ET AL. *A.*

Examiner

Yolanda Wilson

Art Unit

2184

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	L	US-			
	M	US-			

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*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chean et al. A Taxonomy of Reconfiguration Techniques for Fault-Tolerant Processor Arrays. IEEE Computer, pages 55-69.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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